

Electronic Acknowledgement Receipt

EFS ID:	1007449
Application Number:	10801370
Confirmation Number:	3151
Title of Invention:	Direct conversion circuit having reduced bit errors
First Named Inventor:	Takeo Suzuki
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Filer:	Gustavo Siller Jr./Marianne Hall
Filer Authorized By:	Gustavo Siller Jr.
Attorney Docket Number:	9281-4757
Receipt Date:	03-MAR-2006
Filing Date:	16-MAR-2004
Time Stamp:	17:07:09
Application Type:	Utility

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		9281-4757_Status_Request.pdf	82398	yes	2
	Multipart Description				
	Doc Desc		Start	End	
	Transmittal letter		1	1	
	Request for status of Application		2	2	
Warnings:					
Total Files Size (in bytes):			82398		
<p>This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.</p> <p><u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.</p> <p><u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.</p>					